

IN THE SPECIFICATION:

Please amend the paragraph from page 23, line 28 to page 24, line 33 as follows:

--Figure 11 depicts a block diagram of a combined configuration for control logic 94 and I/O ports unit 96, which as mentioned above, I/O ports unit 96 may be included within control logic 94. In Figure 11, internal bus 112 is connected to printer bus 97 for communication with printer CPU 91. Bus 112 is coupled to host computer interface 113 (shown in dashed lines) which is connected to bi-directional line 76 for carrying out bi-directional communication. As shown in Figure 11, bi-directional line 76 may be either an IEEE-1284 line or a USB line. Bi-directional communication line 76 is also coupled to printer interface 74 of host processor 2. Host computer interface 113 includes both IEEE-1284 and USB interfaces, both of which are connected to bus 112 and to DRAM bus arbiter/controller 115 for controlling RAM 99 which includes print buffer 109 (see Figures 9 and 10). Data decompressor 116 is connected to bus 112, DRAM bus arbiter/controller 115 and each of the IEEE-1284 and USB interfaces of host computer interface 113 to decompress print data when processing. Also coupled to bus 112 are line feed motor controller 117 that is connected to line feed motor driver 34a of Figure 9, image buffer controller 118 which provides serial control signals and head data signals for each of print heads 56a and 56b, heat timing generator 119 which provides block control signals and analog heat pulses for each of print heads 56a and 56b, carriage motor controller 120 that is connected to carriage motor driver 39a of Figure 9, and ASF motor controller 125 that is connected to ASF motor driver 41a of Figure 9. Additionally, EEPROM controller 121a, automatic alignment sensor controller 121b and buzzer controller 121c are connected to bus 112 for controlling EEPROM 102, an automatic alignment sensor (generally

represented within sensors 103 of Figure 9), and buzzer 106. Further, auto trigger controller 122 is connected to bus 112 and provides signals to image buffer controller 118 and heat timing generator 119, for controlling the firing of the nozzles of print heads 56a and 56b.--